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**Appendix** 

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## ABSTRACT OF THE DISCLOSURE:

Methods are disclosed for translating or shifting a voltage level of a single ended input. More specifically, the present invention provides a method of translating or shifting a voltage level that doesn't require a complementary input or an additional power supply if the complementary signal isn't available. One embodiment of the method of translating a voltage level of a single-ended input signal using at least one native transistor device having a threshold voltage less than 0V comprises outputting a first voltage level if the single ended input signal is in a first state. A second voltage level is output if the single ended input is in a second state.